Outline

• Introduction
  • Multi-core & Network-on-Chip paradigm
• Performance limitations of conventional planar NoCs
  • Some alternatives
• Possibility of designing wireless NoC (WiNoC)
  • CNT-based antennas
• Possible communication schemes
• Advantages and challenges of WiNoCs
• Summary and road ahead
Multi-core applications

Nokia Sparrow

The $3995 Parallax Propeller QuadRover robot is controlled by the multicore Propeller chip.

Intel LARRABEE
The network-on-chip paradigm

- Driven by
  - Increased levels of integration
  - Complexity of large SoCs
    - New designs counting 100s of embedded cores
  - Need for platform-based design methodologies
  - DSM constraints (power, delay, time-to-market, etc...)
NoC features

- Decoupling of functionality from communication
- Dedicated infrastructure for data transport
NoC limitations

• Predominantly multi-hop communication
  • High Latency and energy dissipation
• Use of Express Virtual Channels

Diagram:
- Core 1
- Core 2
- IP core
- NoC interface
- NoC switch
Novel interconnect paradigms for Multicore designs

Three Dimensional Integration

Optical Interconnects

Lower Latency and Energy Dissipation

Wireless/RF Interconnects
3D NoC

- Stacking multiple active layers
- Manufacturability
  - Mismatch between various layers
  - Yield is currently quite low
- Temperature concerns
  - Despite power advantages, reduced footprint increases power density

Photonic NoC

- High bandwidth photonic links for high payload transfers
- Limitations on switch architecture
  - More than 4-port designs are complex
- On-chip integration of photonic components

On-Chip RF/Wireless Interconnects

• Replace long distance wires
• Use of waveguides out of package or IC structures like parallel metal wires
• Chang et al. demonstrated Transmission Line based RF interconnect for on chip communication
  – Not really wireless
RF NoC

- Bank of high frequency oscillators and filters
- FDM
- On-Chip transmission line acting as data freeways
- Routing of long transmission lines without eliminating any existing links

Wireless Network-on-Chip (WiNoC)

- Use of on-chip wireless links
  - High bandwidth
  - Speed of light
  - Long distance
- Reduce latency and energy dissipation in communication between distant nodes
- Among several options, some may be possible without a revolutionary technology
Early example of on-chip wireless interconnects

• First utilized for distribution of clock signal
  • Technology: 0.18 um CMOS
  • Operating frequency: 15 GHz

• Single Tone
  • Modulation and Channelization is not of any concern

~ 2 mm

Propagation mechanisms of radio waves over intra-chip channels

- Characterization of on-chip radio communications
- Monopole Antennas
- Measurement is done for the frequency range of 10 – 100 GHz

CNT antennas

• To make the antennas small, we need small wavelengths → light (IR, visible, UV)
• MWCNT as Optical Antennae
• Directional radiation characteristics are in an excellent and quantitative agreement with conventional radio antenna theory and simulations

CNT bundle dipole antennas

- SWCNT bundle dipole antennas
- The efficiency of a bundle antenna can be 30–40 dB higher than that of a single SWCNT dipole antenna

Why nanotubes for antenna application?

• Already made by nature! How else would we want to make such small structures?

• Ballistic transport and quantum conductance $\rightarrow$ low resistive loss

• Smooth, defect-free, stable and chemically complete structure $\rightarrow$ no power loss due to defects or edge and surface roughness

• Structural strength and high conductivity $\rightarrow$ high current carrying capacity ($10^9$ A/cm$^2$)
Light absorption and generation in nanotubes

- The CNT is expected to be a linearly polarized dipole radiation source

Conceptual transmitter and receiver

- About 10 different frequency channels are available.
- There is a strong polarization dependence.

Modulation and demodulation are performed by the antenna itself!

Courtesy: Alireza Nojeh, University of British Columbia
Hybrid Wired/Wireless NoC (WiNoC)

• On-chip wireless nodes have associated overhead

• Hybrid architecture
  • Divide the whole NoC into multiple subnets
  • Communication within the subnets is still through traditional wires
  • Utilize wireless links for inter-subnet data exchange
  • Each subnet will have a wireless base station (WB)

• Subnet architectures may vary and even be heterogeneous on the same chip
Network optimization

• Limited Wireless Resources
  • Wireless part of the network should be simple
  • Position of the WB within the subnet is important

• Connectivity in the wireless part of the network
  • Avoid multi-hop communication in the wireless channels
    ➢ Take advantage of speed of light data transfer
  • Point-to-point wireless links
  • Adopt small-world network features
    ➢ Enable easy scalability for larger system sizes

• Minimize the overhead
  • Avoid complicated MAC protocols
Connecting the subnets

• Small-World Nets: The Watts-Strogatz Model
• Establish high speed long distance links among distant blocks on the chip

Figure 1. Watts-Strogatz model interpolates between a regular lattice (left) and a random graph (right). Randomly rewiring just a few edges (center) reduces the average distance between nodes, \( L \), but has little effect on the clustering coefficient, \( C \). The result is a “small-world” graph.

regular lattice   small-world   random graph

Courtesy: Christof Teuscher, Portland State University
Communication mechanisms with CNT antennas

- Use multiband lasers to excite the antennas
  - Electroluminescence phenomenon will eliminate this overhead
- Laser sources of different frequencies
  - Establishes a form of FDM
- Optical modulators/demodulators
- Different frequency channels can be assigned to pairs of communicating subnets
- Antenna elements tuned to different frequencies for each pair
WiNoC with small-world connections

- Embedded Core
- Switch
- Subnet
- Hub
- Wireless link
- Wireline link
- Small World network of hubs
Wireless port

Wireless ports to wireless links

Wireline port to neighboring hub

Input Arbitration /Routing /Output Arbitration

Wireline port to subnet

Antenna

MZM electro-optic modulator

TDM modulator

Data flit to be transmitted

TDM demodulator

Data flit received

LNA

Transmitter

Receiver
Overall channelization scheme

- 32-bit flit width
- 4 distinct frequency channels
- Combination of FDM and TDM.
- Simple on-off keying
Establishing wireless links
Throughput and Latency

WiNoC is capable of improving performance of wireline architectures
Scaling trend

Throughput degrades more if the subnet size is increased rather than increasing the number of subnets.
Summary

• WiNoCs are promising alternatives to conventional planar on-chip networks
• Capable of improving NoC performance significantly
• CNTs demonstrate interesting optical antenna properties
• WiNoCs designed with CNT antennas will have low overhead.
Road ahead

• Overall network design
  • Development of scalable wireless network
  • Network optimization
    • Partitioning of wireless and wired network
  • Reliability of the wireless channel
    • Novel ECC schemes

• CNT antennas are promising
  • But some unknowns!

• Explore the possibility of NoC with mm-wave wireless links
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