

CPE300: Digital System Architecture and Design

Fall 2011

MW 17:30-18:45 CBC C316

Course Info

- <http://www.egr.unlv.edu/~b1morris/cpe300>
- MW 17:30-18:45 CBC C316
- Final: Wed 12/14 18:00-20:00

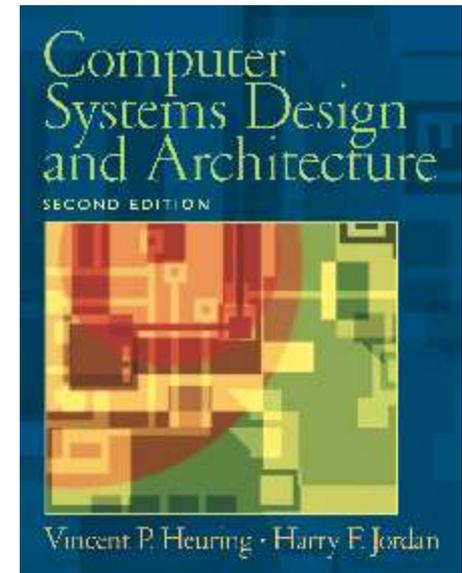
Professor Info

- Brendan Morris
- brendan.morris@unlv.edu
- Office Hours: SEB 3243, MTuW 16:30-17:30
- Phone: 702-774-1480

Textbook

- Computer Systems Design and Architecture, V.P. Heuring and H. F. Jordan, 2nd Edition
- ISBN: 0-13-048440-7

- Additional References
- Computer Organization and Design: The Hardware/Software Interface, D.A. Patterson and J. L. Hennessy



Grading Policy

- Midterm Exam: 25% 10/19
 - Final Exam: 30% 12/14
 - Project: 20% 12/5
 - Homework: 25% (Semi-)Weekly
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- Homework is a large part of your grade
 - Assignments posted on the website
 - Exam questions will be based on HW
 - Be sure you understand solutions
 - You will be successful if you understand the HW
 - Project will be microcontroller programming

Notes on Assignments

- Students are encouraged to study together in groups but all assignments must be completed individually.
 - This means no sharing or copying
 - Plagiarism will not be tolerated and will result in a fail for the class
- Homework due in class on designated date
 - No late assignments are accepted
- If you will miss class for some reason provide early warning (notification by 9/2)
 - E.g. for religious holidays or sickness

Important Dates

- 08/29/11 Instruction Begins
- 09/02/11 Add/Drop Deadline (100% refund)
- 09/05/11 Labor Day Holiday
- 10/07/11 Drop Deadline (50% refund)
- 10/19/11 Midterm
- 11/07/11 Drop Deadline (0% refund)
- 12/02/11 Project Due
- 12/14/11 Final Exam